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Total Number of Pages: 02 4th Semester Regular Examination 2017-18 MCA MCA402 COMPUTER GRAPHICS AND MULTIMEDIA BRANCH: MCA Time: 3 Hours Max Marks: 100 **Q.CODE** : C696 Answer Part-A which is compulsory and any four from Part-B. The figures in the right hand margin indicate marks. Answer all parts of a question at a place. Part - A (Answer all the questions) Q1 Answer the following questions: multiple type or dash fill up type: (2×10) The number of pixels stored in the frame bufferof agraphics system is known as a) b) On a black and white system with one bit per pixel, the frame buffer is commonly called as The process of digitizing a given picture definition into a set of pixel-intensity for storage in the frame buffer is called The projection in which the plane normal to the projection makes equal angle to the three axes is called The process of expanding or compressing the dimension of an object is called e) The algorithm which displays line type attribute by plotting pixel spans is called f) The selection and separation of the part of a text or image for further processing ig called ____ For a Cavalier Projection the foreshortening factor is h) The Range that specifies the Grey or Grey scale level is i) How many steps process for creating a 3D animation? j) Answer the following questions: Short answer type: (2×10) What is the difference between Random scan and Raster scan system? Q2 a) What is staircase effect? What is shadow -mask technique? b) What is composite transformation? c) d) What is window and viewport? What is the difference between orthographic and oblique projection. What is HCS, Why it is used in computer Graphics. f) Draw the taxonomy of Projection. g) h) What is key frame? j) What is Hypertext and Hypermedia? Part - B (Answer any four questions) Describe with diagram the architecture and working of DVST. (10) Consider 3 different raster system with resolution of 640×480,1280×1024, and (5) Consider 3 different factors and 2560×2048, what size of frame buffer is needed (in bytes) for each of these Q3 systems to store 12 bits per pixel.?

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